

71711-2-8

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:
Tai-Chong CHAI et al.

) Patent Group Art Unit:

Serial Number: To be Assigned

) Examiner:

Filed:

LEAD FRAME FOR AN INTEGRATED
CIRCUIT CHIP (Small Window)

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NEW UTILITY PATENT APPLICATION TRANSMITTAL

Washington, DC
7 February, 2000

Honorable Assistant Commissioner for Patents
Box Patent Applications
Washington, DC 20231

Sir:

Please enter a new patent application, referencing **Attorney Docket Number**

71711-2-8.

Transmitted herewith please find:

1. ☐ Fee Transmittal Form (2 copies)

2. ☒ Specification

☒ Descriptive title of the Invention

☐ Cross References to Related Applications

☐ Statement Regarding Fed sponsored R&D

jc714 U.S. PTO
02/07/00

09497421.020700

jc564 U.S. PTO
09/497421
02/07/00

[illegible]

X Brief Summary of the Invention

x Detailed Description

X Abstract of the Disclosure

4. ☐ Oath or Declaration

☐ Discrete Dec

5. ☐ Microfiche Computer Program (Appendix)

☐ Computer Readable Copy

☐ Statement verifying identity of above copies

7. ☐ Assignment Papers (cover sheet & document)

9. ☐ English Translation Document

X Copies of IDS Citations

11. ☐ Preliminary Amendment
12. ☒ Return Receipt Postcard (MPEP 503)
13. ☒ Certified Copy of Priority Document
14. ☐ Other
15. Fee Calculation (Large Entity)

(14 Claims -20) X 18 = \$ 0

(4 Independent Claims - 3) X 78 = \$ 78

Multiple Dependent Claims X 260 = \$ 0

Basic Fee \$ 690.00

Total \$ 768.00

A check in the amount of \$ 768.00 is enclosed.

18. Correspondence Address

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Respectfully submitted,

Michael Bergman, Esq.
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PATENT APPLICATION SERIAL NO. _____

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICE
FEE RECORD SHEET

02/16/2000 AGRAY1 00000012 09497421

01 FC:101	690.00	OP
02 FC:102	78.00	OP